

Multilayer Ceramic Capacitors (High dielectric type)

UMK212F224ZD-T



■ Features

- Item Summary
0.22uF+80%/-20%, 50V,
Y5V/F, 0805/2012 (EIA/JIS)
- Lifecycle Stage
Phaseout
- Standard packaging quantity (minimum)
Taping Paper 4000pcs

■ Products characteristics table

Capacitance	0.22 uF +80%/-20%
Case Size (EIA/JIS)	0805/2012
Rated Voltage	50 V
tan δ (max)	7 %
Temperature Characteristic (EIA)	Y5V
Operating Temp. Range (EIA)	-30 to +85 °C
Temperature Characteristic (JIS)	F
Operating Temp. Range (JIS)	-25 to +85 °C
High Temperature Loading (% Rated Voltage)	200 %
Insulation Resistance (min)	500 M Ω · μ F
RoHS Compliance (10 subst.)	Yes
REACH Compliance (205 subst.)	Yes
Halogen Free	Yes
Soldering	Reflow/Wave

■ External Dimensions

Dimension L	2.0 ±0.10 mm
Dimension W	1.25 ±0.10 mm
Dimension T	0.85 ±0.10 mm
Dimension e	0.50 ±0.25 mm

2020.05.02

The data is reference only. Electrical characteristics vary depending on environment or measurement condition.
 TAIYO YUDEN reserves the right to make change to the data at any time without notice.
 Before making final selection, please check product specification.

Multi Layer Ceramic Capacitor

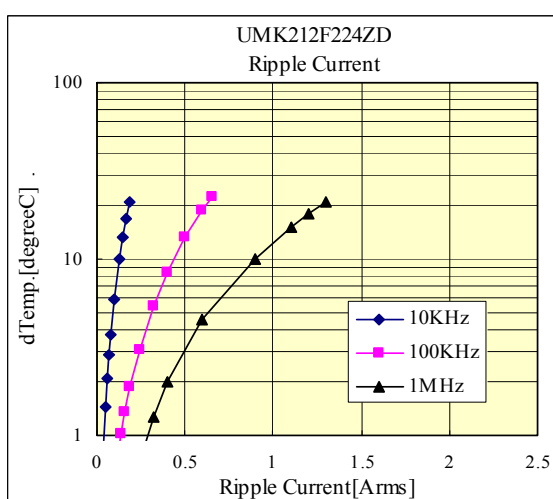
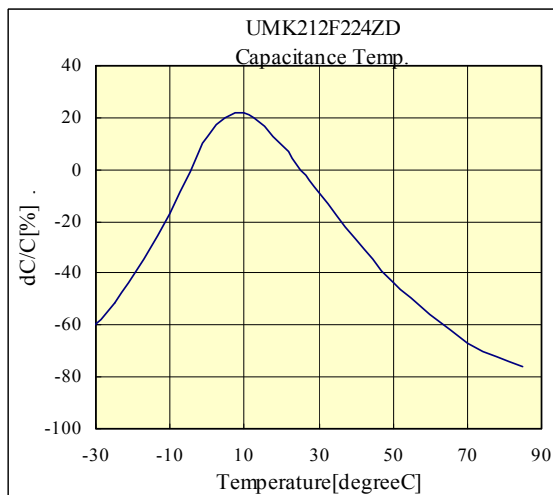
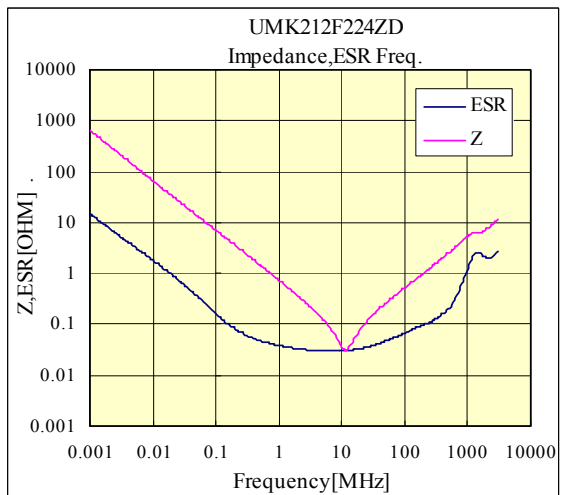
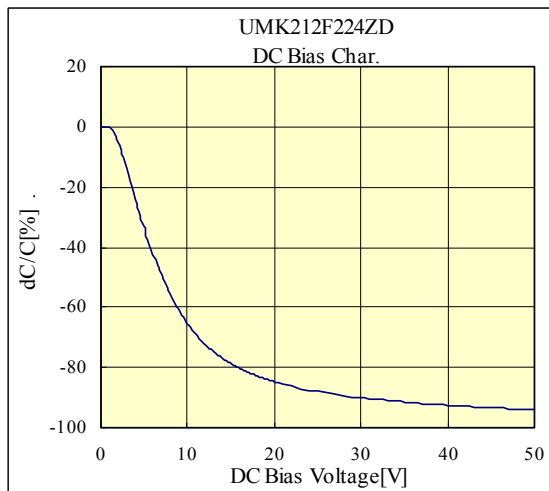
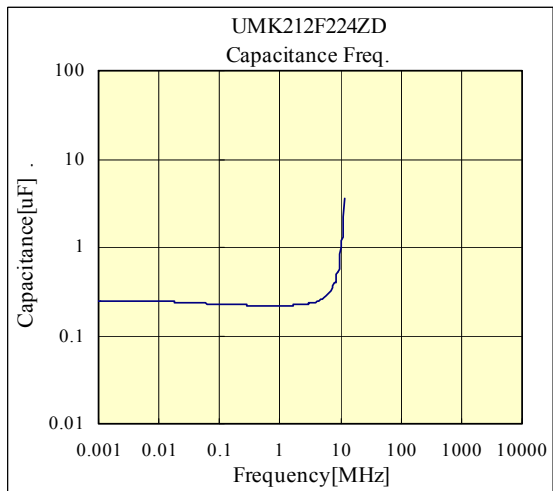
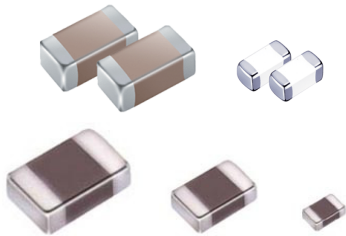
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-Electrical Characteristics Data- Aug-2010

RoHS compliance product

○UMK212F224ZD

50V 2012 F/Y5V 0.22 μ F +80/-20% t=0.95mmMAX
50V 0805 F/Y5V 0.22 μ F +80/-20% t=0.037inchMAX



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