

Passive module - FLKM 16/DI/SI/LA/DV - 2304458

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Fuse module with LEDs (for digital input cards of the Delta V control system)

Product Features

- These system-specific interface modules for DeltaV modules are used in combination with the relevant system cables
- 1:1 connection
- LED status indicator per signal path
- 5 x 20, 50 mA T, IEC 60127-2/3 fuse per channel



Key Commercial Data

Packing unit	1 pc
Weight per Piece (excluding packing)	219.1 g
Custom tariff number	85369010
Country of origin	Germany

Technical data

Dimensions

Width	90 mm
Height	90 mm
Depth	68 mm

Ambient conditions

Ambient temperature (operation)	-20 °C ... 50 °C
Ambient temperature (storage/transport)	-20 °C ... 70 °C

General

Max. perm. operating voltage	30 V DC
Max. perm. current (per branch)	50 mA (In delivered state, with one 50 mA fuse, max. 1 A permitted)
Number of positions	16

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Technical data

General

Status display	Yes
Mounting position	any
Assembly instructions	In rows with zero spacing
Degree of protection	IP00
Standards/regulations	DIN EN 50178
	IEC 60664
	IEC 62103
Rated insulation voltage	50 V
Rated surge voltage	0.5 kV (Functional insulation)
Degree of pollution	2
Overvoltage category	II

Connection data for connection 1

Connection name	Field level
Connection in acc. with standard	IEC / EN
Connection method	Screw connection
Stripping length	8 mm
Screw thread	M3
Conductor cross section solid	0.2 mm ² ... 4 mm ²
Conductor cross section flexible	0.2 mm ² ... 2.5 mm ²
Conductor cross section AWG	24 ... 12

Connection data for connection 2

Connection name	Controller level
Connection method	IDC/FLK pin strip (2.54 mm)
Number of connections	1
Number of positions	16

Supported controller

Controller	Emerson DeltaV
- suitable I/O card	VE4001S2T2B3 Series 2

Standards and Regulations

Standards/regulations	DIN EN 50178
	IEC 60664
	IEC 62103
Degree of pollution	2
Overvoltage category	II

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Classifications

eCl@ss

eCl@ss 4.0	27250313
eCl@ss 4.1	27250313
eCl@ss 5.0	27250313
eCl@ss 5.1	27250313
eCl@ss 6.0	27242208
eCl@ss 7.0	27242208
eCl@ss 8.0	27141152

ETIM

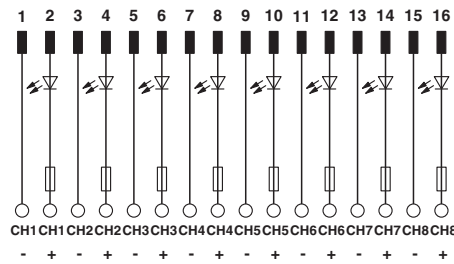
ETIM 2.0	EC001423
ETIM 3.0	EC001423
ETIM 4.0	EC001423
ETIM 5.0	EC002780

UNSPSC

UNSPSC 6.01	30211824
UNSPSC 7.0901	39121421
UNSPSC 11	39121421
UNSPSC 12.01	39121421
UNSPSC 13.2	39121421

Drawings

Circuit diagram



FLKM 16/DI/SI/LA/DV connection scheme